



## ARTEMIS (ETP) Joint Undertakings<sup>i</sup>

*“Advanced Research and Technology for Embedded Intelligence and Systems”*

The industrial partners within ARTEMIS stress the importance of application-oriented research that supports the development of sophisticated prototypes of embedded systems so as to provide proofs of concepts for novel applications. These implementation developments are needed to empirically validate design requirements and allow real-time performance evaluation of novel designs and architectures.

To address this needs, the ARTEMIS-Joint Undertakings defines eight **Sub-Programmes**:

ASP1. Methods and processes for safety-relevant embedded systems

ASP2. Person-centric health management

ASP3. Smart environments and scalable digital services

ASP4. Efficient manufacturing and logistics

ASP5. Computing environments for embedded systems

ASP6. Security, privacy and dependability

ASP7. Embedded technology for sustainable urban life

ASP8. Human-centric design of embedded systems

One of the major characteristics of the new research approach promoted by the ARTEMIS JU is the promotion of cross-fertilization and reuse of technology results in different application domains.

The general objectives of the Programme are:

- reduce the cost of the system design from 2005 levels by 15%;
- achieve 15% reduction in development cycles;
- manage a complexity increase of 25% with 10% effort reduction;
- reduce the effort and time required for re-validation and recertification after change by 15%;
- achieve cross-sector reusability of Embedded Systems devices developed using the ARTEMIS JU results.



Along with 3 **industrial priorities**:

1. Reference designs and architectures: reference designs and architectures that offer common architectural approaches for given ranges of applications.
2. Seamless connectivity and middleware: to allow seamless connectivity and interoperability.
3. Design methods and tools: Integrated system design methods and tools for rapid development and prototyping.

In contrast and complementary to the EC's [Framework Programme 7 \(FP7\)](#), ARTEMIS focuses on downstream **research and technology development with a strong market drive**. The sub-programmes provide focus of technology development to market areas that are strategic for European competitiveness.

## **SMEs in ARTEMIS**

ARTEMIS recognises and welcomes the participation of all classes of SMEs relevant to Embedded Systems, being providers of basic embedded technology, intermediate players who adapt generic or cross domain technology to a particular market and also service providers.

The ARTEMIS Strategic Research Agenda sets out a target of having *50% more European SMEs* within the support of ARTEMIS Joint Undertakings than there were in 2005 engaged in the Embedded Systems supply chain, from concept through design and manufacture, delivery and support, project consortia must be balanced, considering explicitly the involvement of SMEs and favouring clustering of SMEs in innovation eco-systems.

Project proposals should clearly indicate concrete and quantifiable measures to assist participating SMEs in their dissemination of public project results and subsequent valorisation of the results in near-future business plans.

## **Key Points**

- Projects should address at least one [sub-programme](#) at least one of the [Industrial Priorities](#).
- Each proposal should include demonstration of core technological developments in order to achieve the empirical validation expected.
- Large, strategic initiatives are encouraged, complemented with smaller more focussed research proposals.
- Projects addressing [ASPs 2, 4 and 6](#) are especially welcome.



- Projects are expected to have a strong application focus. At the same time, projects must make explicit contributions to the [industrial priorities](#).
- Projects should demonstrate the added value of cooperation between partners
- each sub-programme will generate a small number of large project proposals, complemented by a number of smaller footprint initiatives
- Projects are not expected to last more than 3 years
- Projects should involve at least 3 independent legal entities established in at least 3 ARTEMIS Member States<sup>ii</sup>

## Call for Proposals

A two-stage procedure will be followed: proposers must submit a Project Outline (PO), followed by the submission of Full Project Proposals (FPP).

- Deadline for submission of Project Outlines: 15th April 2009 at 17:00:00 (Brussels local time)
- Deadline for submission of Full Project Proposals: 3rd September 2009 at 17:00:00 (Brussels local time)

## Indicative Budget of Call 2009

ARTEMIS Joint Undertaking	€1 37.086.500
ARTEMIS Member States	€ 67.430.000
Total indicative budget of Call	€104.516.500

For more information on how to apply read the [information package](#).

For more support visit [https://www.artemis-ju.eu/call\\_2009#support](https://www.artemis-ju.eu/call_2009#support) or contact [innovation@gle.co.uk](mailto:innovation@gle.co.uk)

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<sup>i</sup> This document is for guidance only. We strongly recommend applicants to visit the ARTEMIS website and read all documentation provided.

<sup>ii</sup> Currently: (Austria, Belgium, Cyprus, Czech Republic, Denmark, Spain, Estonia, Finland, France, Germany, Greece, Ireland, Italy, Hungary, Latvia, the Netherlands, Norway, Portugal, Romania, Slovenia, Sweden and the United Kingdom)